IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Hans-Joachim Müssig

Attorney Docket No.: 536-009.13

Application No.: To be assigned

Group No.: To be assigned

Filed: Herewith

Examiner: To be assigned

For:

SEMICONDUCTOR CAPACITOR AND MOSFET FITTED THEREWITH

Commissioner for Patents Director of the U.S. Patent & Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant submits herewith references of which they are aware, which they believe may be material to the examination of this application and in respect of which they may have a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any document referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined under 37 CFR 1.56(a) exists.

> I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

(IDS Transmittal - Page 1 of 2)

Attorney Docker No. 536-009.13 Serial No. (to be assigned)

This IDS is being submitted simultaneously with the request for entry into the U.S. national phase corresponding to PCT application number PCT/EP03/07179 having international filing date of July 4, 2003. Therefore, the undersigned respectfully submits that no fee is due for filing this IDS. Should any fees be due of which the undersigned is unaware, the Commissioner is hereby authorized to charge deposit account 23-0442 any fee deficiency required to submit this IDS.

A PTO-1449 with cited references is enclosed.

Respectfully submitted,

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Sheet 1 of 1

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				ATTY DOCKET NO. SERIAL NO. 10/520396 To be assigned			
				APPLICANT: Hans Joachim Müssig			
				FILING DATE: Herewith	ART UNIT	: To be assign	ned
			UNITED ST	ATES PATENT DOCUMEN	TS		North American
EXAM. INITIAL		DOCUMENT NUMBER	ISSUE/PUBL DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF
	US	2002/0036313	3-28-2002	Sam Yang et al.			
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	wo	02/097895	12-5-2002	Dietmar Krüger et al.			abstract only
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		Germany; received 8 2001 IRW Final Rep), HJ. Müssig 3 September 20 ort, 0-7803-7	g et al., IHP, Im Technologi 001, accepted for publicat 167-4/01/2001 IEEE, Can Silicon Integrated Circuits	iepark 25, l ion 3 Dec. Praseodymi	0-15236 Frank 2001. ium Oxide be a	efurt (Oder),
xaminer		formation on Si(001 Germany; received 8 2001 IRW Final Rep High-K Gate Dielecte), HJ. Müssig 3 September 20 ort, 0-7803-7 ric Material for	et al., IHP, Im Technologi 001, accepted for publicat 167-4/01/2001 IEEE, Can	iepark 25, l ion 3 Dec. Praseodymi	0-15236 Frank 2001. ium Oxide be a	efurt (Oder),